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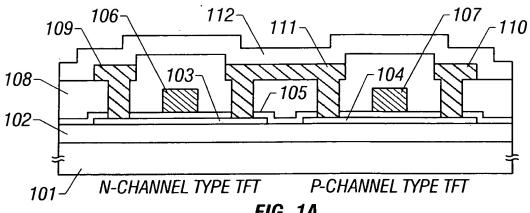


FIG. 1A (Prior Art)

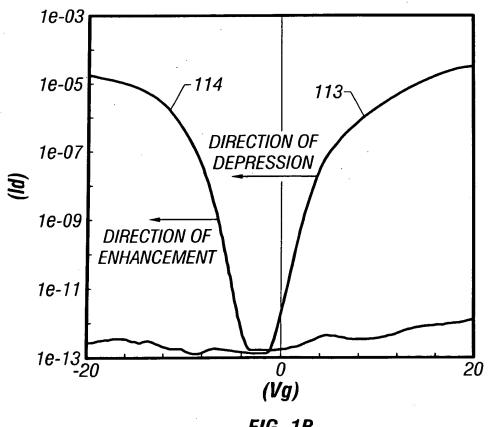
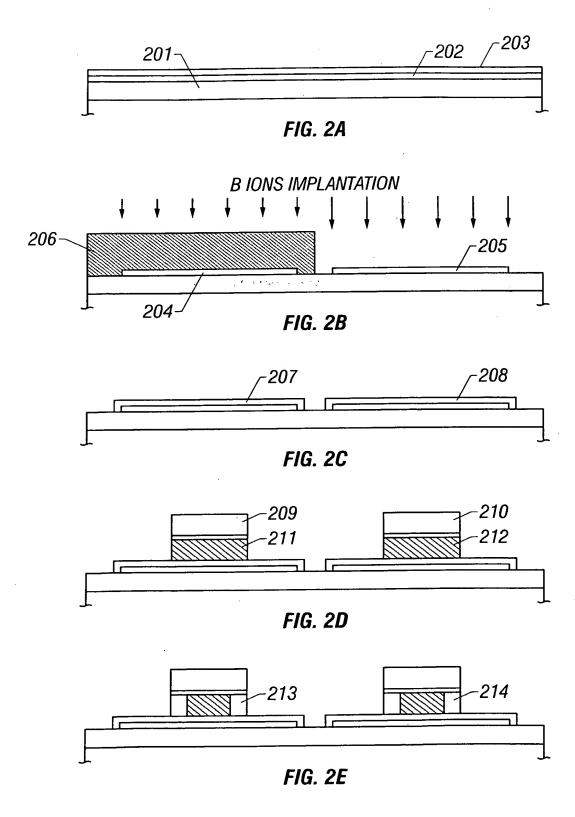
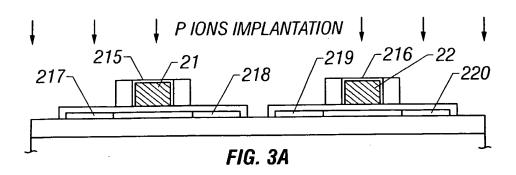
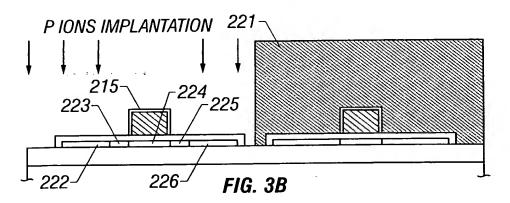


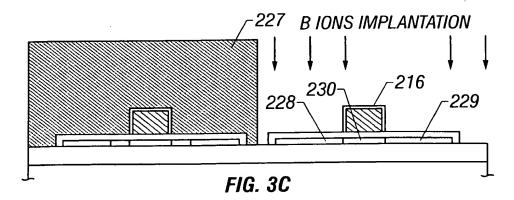
FIG. 1B (Prior Art)

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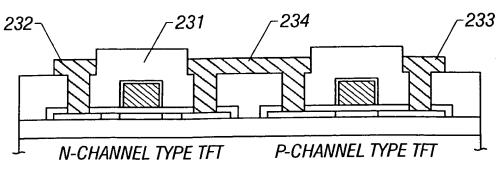
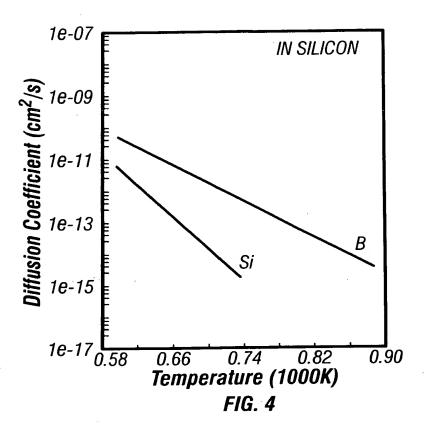
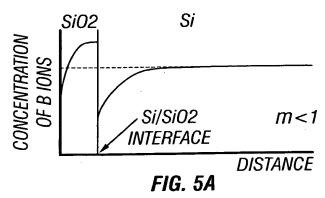
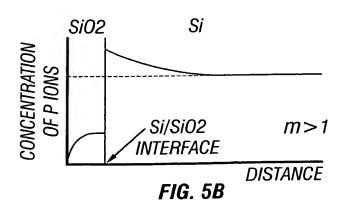


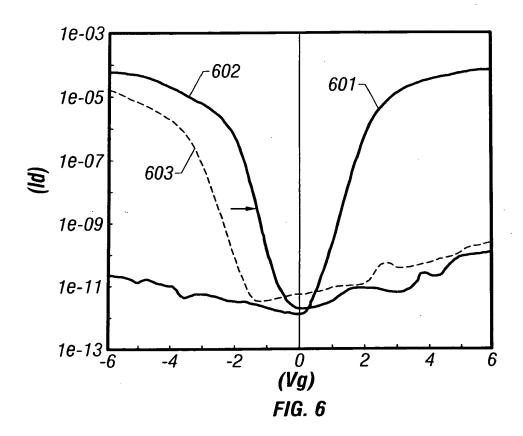
FIG. 3D

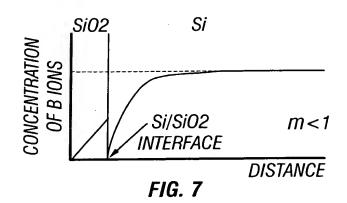












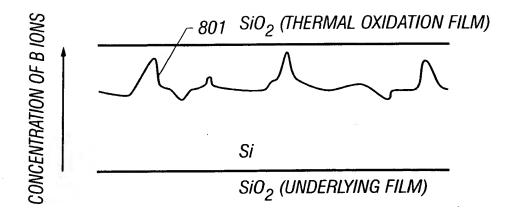


FIG. 8A

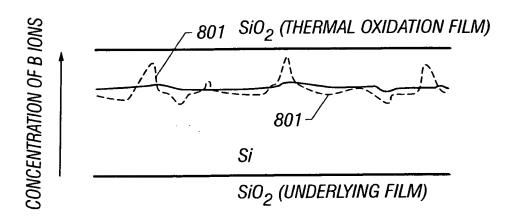


FIG. 8B

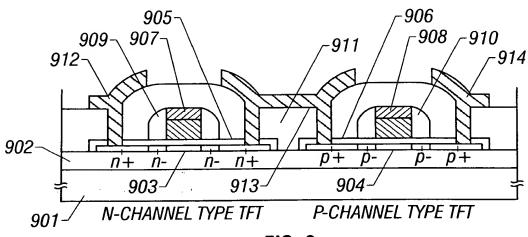


FIG. 9

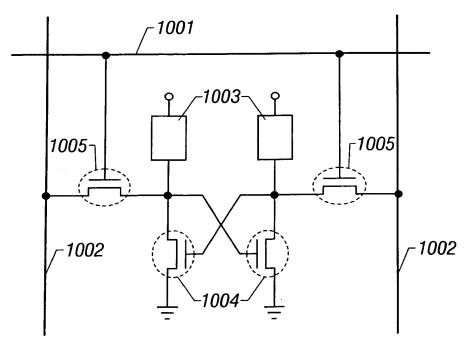
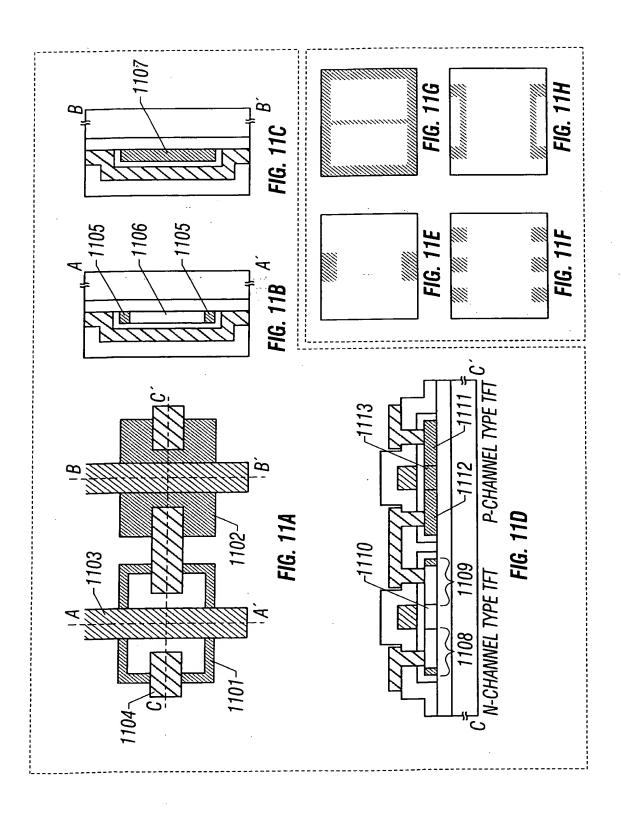
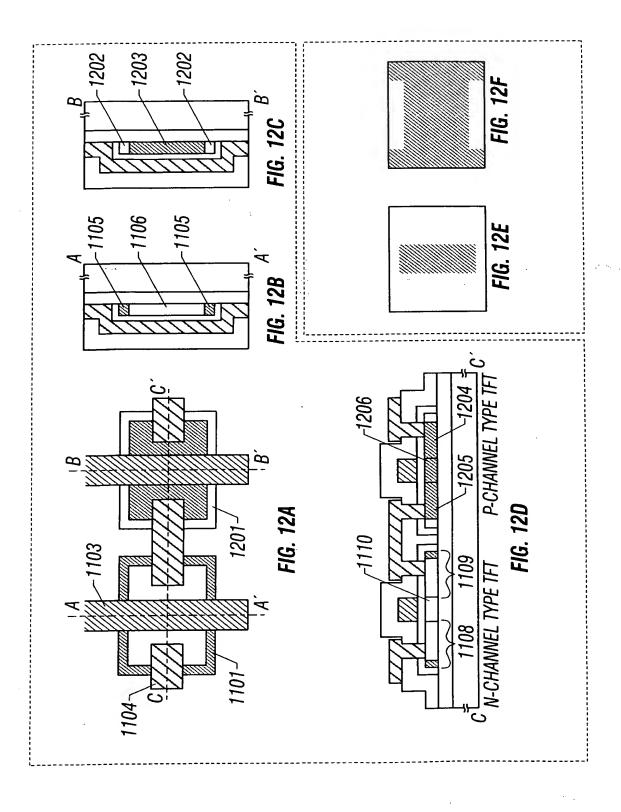


FIG. 10



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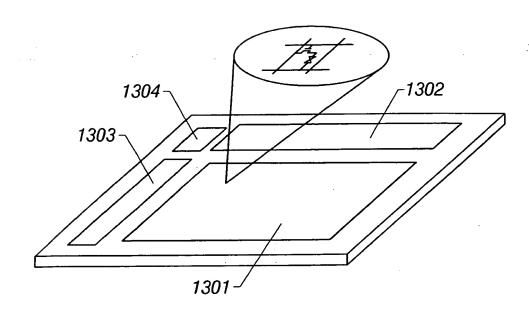


FIG. 13

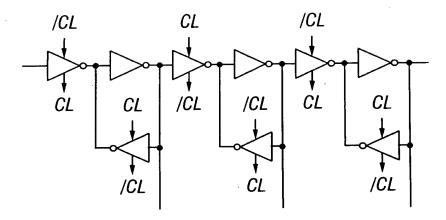


FIG. 14A

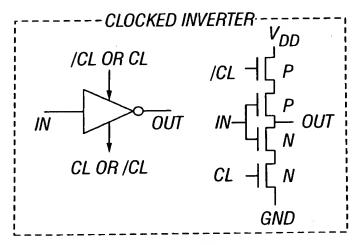


FIG. 14B

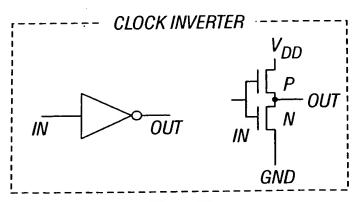
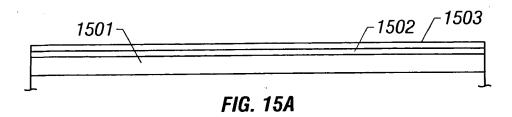
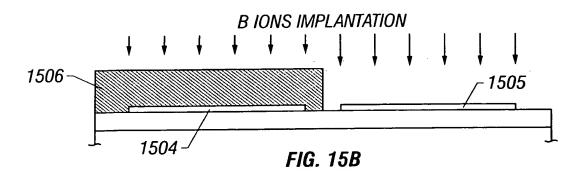
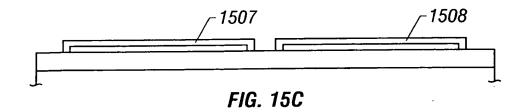


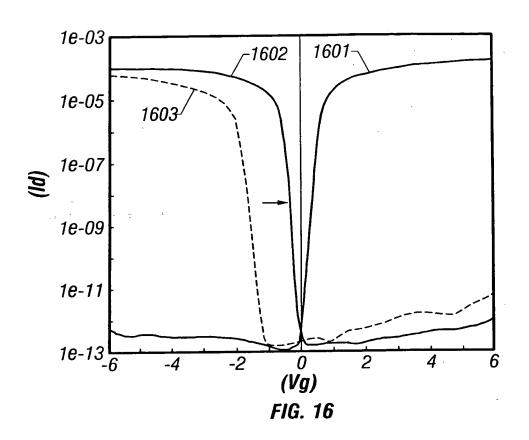
FIG. 14C





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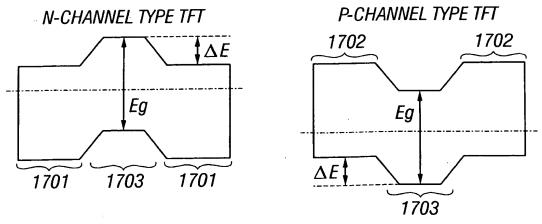
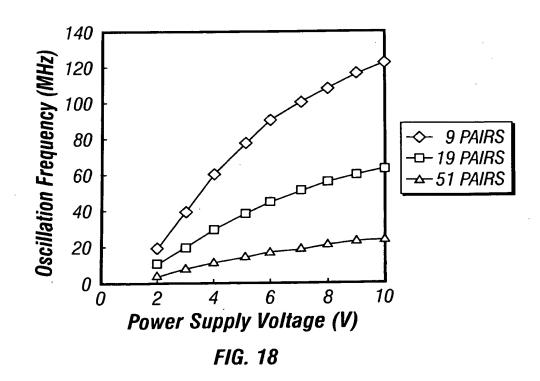


FIG. 17

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TFT SIZE Pch 3/20 (L/W [um]) Nch 3/20

|      | Oscillation Frequency (MHz) |             |          |
|------|-----------------------------|-------------|----------|
| (V)  | 9 Pairs                     | 19 Pairs    | 51 Pairs |
| 2.0  | 18.6                        | 9.6         | 3.6      |
| 3.0  | 38.5                        | 19.2        | 7.2      |
| 4.0  | 59.8                        | 29.4        | 10.7     |
| 5.0  | 75.7                        | 37.2        | 13.9     |
| 6.0  | 90.4                        | 44.8        | 16.6     |
| 7.0  | 100.3                       | <i>50.8</i> | 18.2     |
| 8.0  | 108.0                       | <i>55.3</i> | 20.7     |
| 9.0  | 117.0                       | <i>59.4</i> | 22.4     |
| 10.0 | 123.0                       | 62.6        | 23.6     |



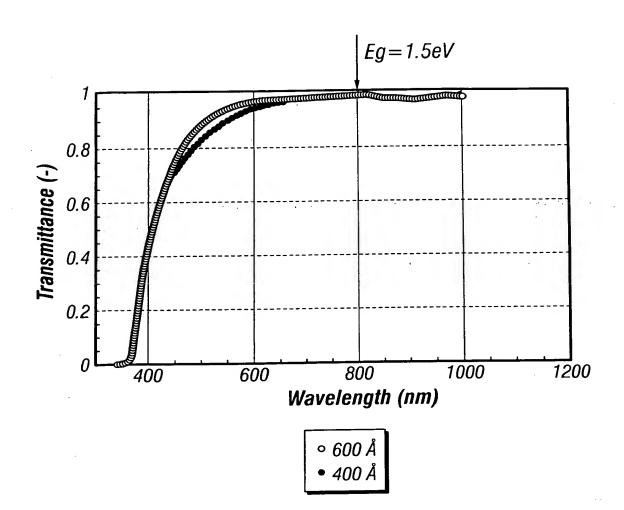
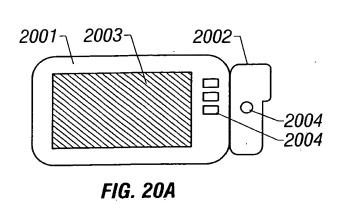
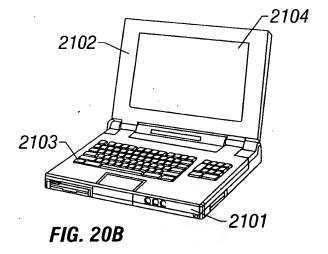
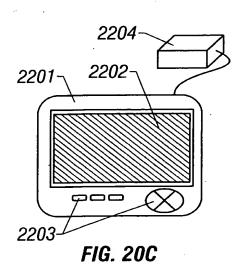


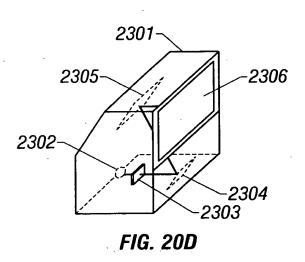
FIG. 19











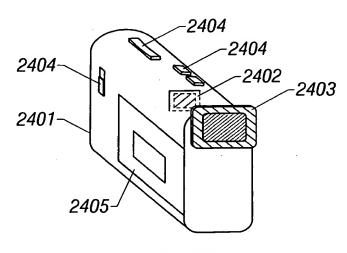


FIG. 20E